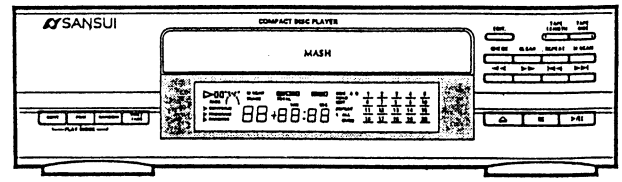
SERVICE MANUAL

SANSUI

CD-X950

MINI COMPONENT SYSTEM
<COMPACT DISC PLAYER>



Organization of System Component

System Name	Amplifier	Tuner	CD Player	Cassette Deck	Speaker
MC-X950/X950L	A-X950	T-X950/X950L	CD-X950	D-X950	S-X950
MC-X750/X750L	A-X750	T-X950/X950L	CD-X950	D-X950	S-X750

Notes: 1. The CD-X950 comes in two colors. Black or Gold.
2. For packing material and accessory parts, see the service manual of the A-X950 or A-X750.

CAUTION
1. Parts identified by the ⚠ symbol on the schematic diagram and the parts list are critical for safety. Use only replacement parts that have critical characteristics recommended by the manufacturer.
2. Make leakage-current or resistance measurements to determine that exposed parts are acceptably insulated from the supply circuit before returning the appliance to the customer.

• Specifications

Format.....Compact disc, digital audio system
Pickup.....3-Beam, semiconductor laser
Decoding (D/A).....1-Bit / MASH
(MASH is a trademark of NTT)
Frequency response.....20 Hz to 20 kHz, ±0.5 dB
Total harmonic distortion.....Less than 0.005% (1 kHz)
Signal-to-Noise ratio.....Better than 100 dB
Dynamic range.....Better than 93 dB
Wow & flutter.....Below measurable limit
Dimensions.....270 mm (10-11/16") W
75 mm (3") H
280 mm (11-1/16") D
Weight.....2.2 kg (4.9 lbs) net

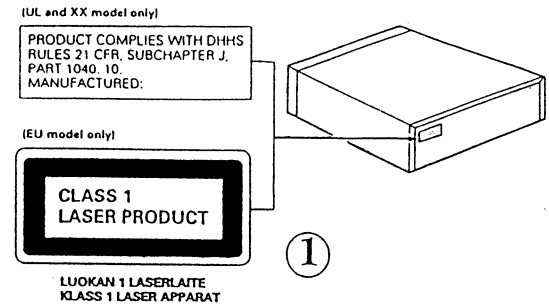
CAUTION: USE OF CONTROLS FOR ADJUSTMENTS OR PERFORMANCE OF PROCEDURES OTHER THAN THOSE SPECIFIED HEREIN MAY RESULT IN HAZARDOUS RADIATION EXPOSURE.
THE COMPACT DISC PLAYER SHOULD NOT BE ADJUSTED OR REPAIRED BY ANYONE EXCEPT PROPERLY QUALIFIED SERVICE PERSONNEL.

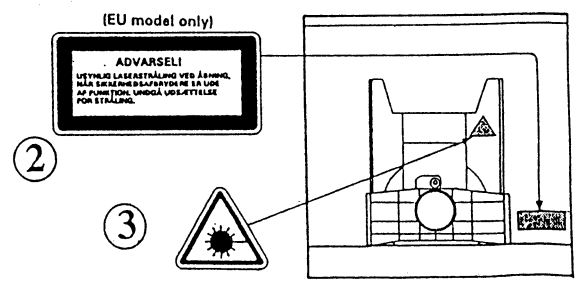
- The following label has been affixed to the unit, listing the proper procedure for working with the laser beam.

1. THIS LABEL IS ATTACHED TO THE PLACE AS ILLUSTRATED TO INFORM THAT THE APPARATUS CONTAINS A LASER COMPONENT.
2. THIS LABEL IS ATTACHED TO THE INSIDE OF THE TOP COVER AS ILLUSTRATED, SO THAT ANY FURTHER PROCEDURE WILL BRINGING THE USER INTO EXPOSURE WITH THE LASER BEAM.
3. THE WARNING LABEL INFORMING OF RADIATION IS PLACED INSIDE THE UNIT. AS SHOWN IN THE ILLUSTRATION TO WARN AGAINST FURTHER MEASURES ON THE UNIT. THE EQUIPMENT CONTAINS A LASER COMPONENT RADIATING LASER RAYS EXCEEDING THE LIMIT OF LASER PRODUCTS OF CLASS 1.

UL---------- Manufactured for U.S.A. market.
XX---------- Standard Version.
EU---------- Manufactured for European market.

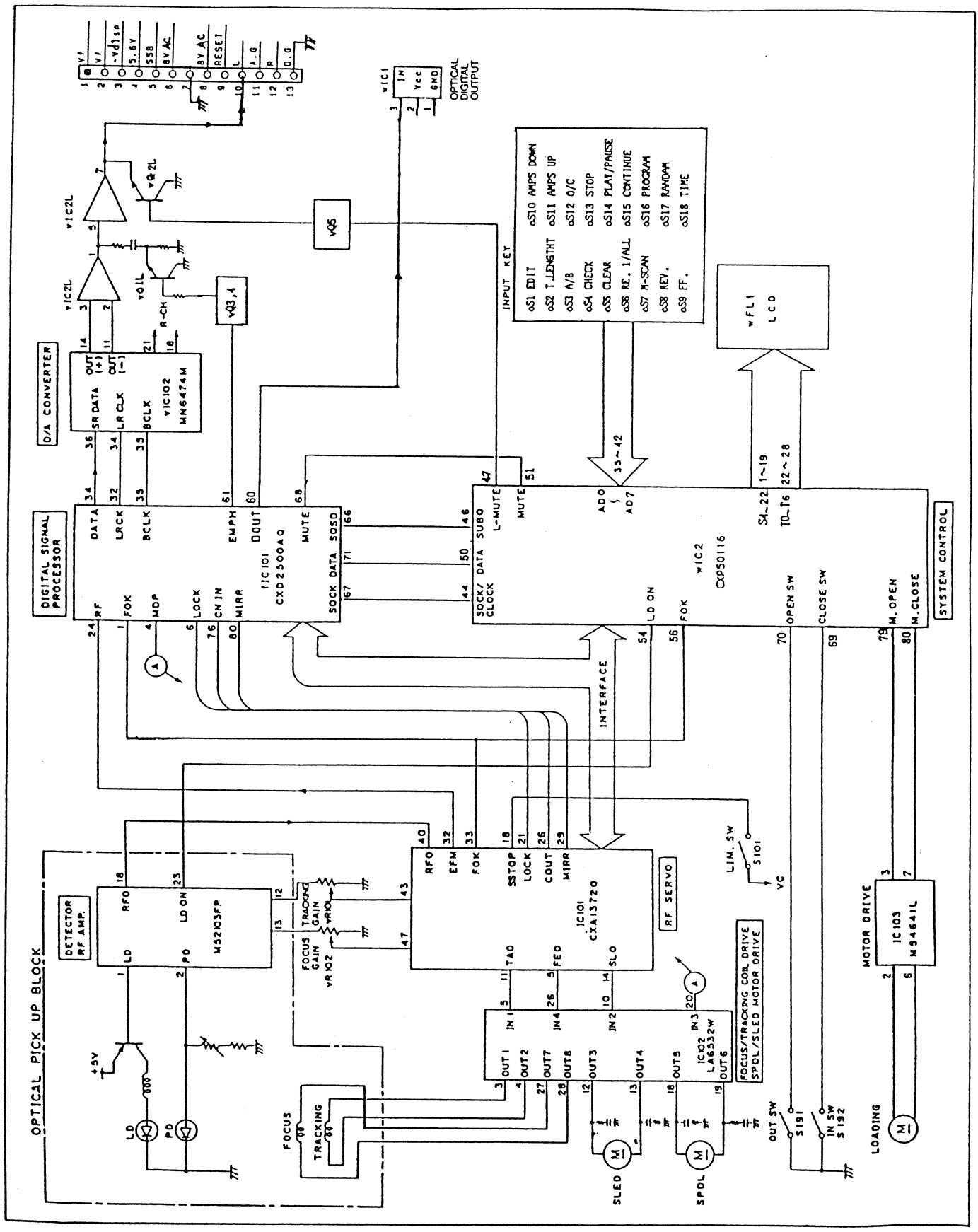
(UL and XX model only)





Optical pickup :
Laser output : max. 0.5 mW
Wavelength : 760~800 nm
Diode material : GaAlAs

1. BLOCK DIAGRAM



2. PARTS LIST OF BOARD

Notice for Parts

1. Some printed circuit board are not supplied assembled. To separate these in this Service Manual, the stock numbers are not indicated for these boards. However, stock numbers for individual parts are indicated.
2. Since some capacitors and resistors are omitted from parts lists in this Service Manual, refer to the Common Parts List for capacitors and resistors, which was issued on June 1987.
3. Abbreviations in this Parts List are as follows.

•Abbreviations List

SW.	: Switch	C.C.	: Ceramic Capacitor

2-1. F-6800 Main Board <Stock No. 01319804>

Parts No.	Stock No.	Description
•FET		
fFT1	46643800	2SJ103-Y
	or 46643801	2SJ103-GR
•IC		
fIC1	49666300	CXD2500AQ
fC2	48748400	0.1µF 50V C.C.
•Transistor		
⚠ mQ1	49349500	2SB1375
mQ2	46367301	2SC2458
or	48058801	2SC1740S
•IC		
⚠ mIC1	49420800	NJM7806FA
⚠ mIC2	49474800	NJM7906FA
•Diode		
mD1	46464100	1SS133
⚠ mD2	03117000	RB152-LFF
⚠ mD3	48123600	11E2
⚠ mD4	48123600	11E2
mD5	46464100	1SS133
•Zener Diode		
⚠ mDZ1	48553300	MTZ7.5B
⚠ mR1,2	46227400	4.7Ω 1/2W N.I.R.
mC4	48748400	0.1µF 50V C.C.
mC5	48748400	0.1µF 50V C.C.
⚠ mF1	49710500	Fuse 1.0A 50V
⚠ mF2	49710500	Fuse 1.0A 50V
qZ5	49718800	13P FG System Control Socket
•Transistor		
vQ1	46604301	2SC3327
vQ2	46604301	2SC3327
vQ3	46367201	2SA1048
or	48058601	2SA933S
vQ4	46367301	2SC2458
or	48058801	2SC1740S
vQ5	46367201	2SA1048
or	48058601	2SA933S
•FET		
vFT1	46724700	2SK241-Y
or	46724701	2SK241-GR
•IC		
vIC1	49543400	MN6474M
or	49775100	MN6474A
vIC2	03607700	NJM4558D
or	49507200	RC4558D
vXO1	49357400	Quartz Element HC-49/U
or	49771500	Quartz Element
or	49771600	Quartz Element
•Diode		
vD1	46464100	1SS133
vD2	46464100	1SS133
vC1	48748400	0.1µF 50V C.C.
vC4	48748400	0.1µF 50V C.C.
vC8	48748400	0.1µF 50V C.C.
vL1	48289400	Inductor 10µH
•Transistor		
wQ1	46367301	2SC2458
or	48058801	2SC1740S
•IC		
wIC1	49664500	OPT Module
wIC2	49731800	CXP50116
wXO1	49488500	Quartz Element, CSA4.19MG
wC1	48748400	0.1µF 50V C.C.
wC8	48748400	0.1µF 50V C.C.
wL1	48289400	Inductor 10µH

2-2. F-6801 Control Switch Board

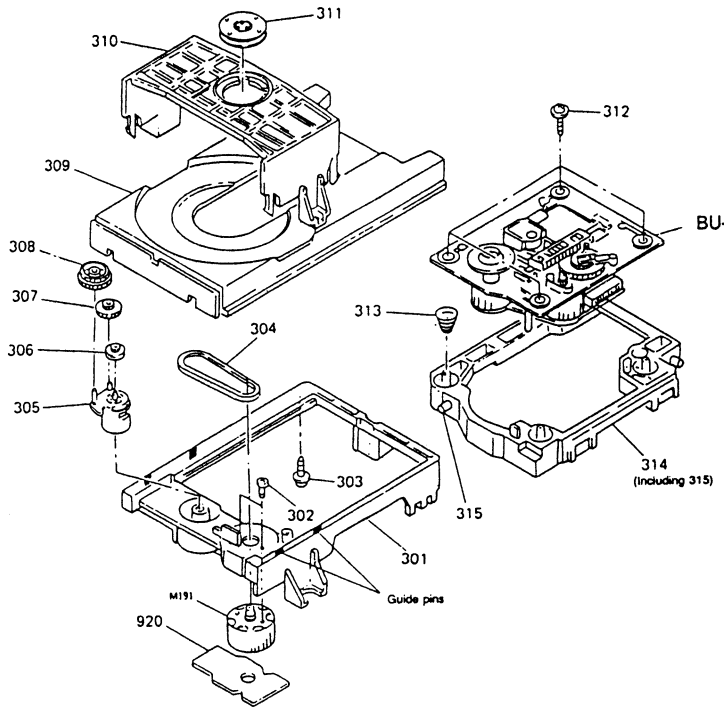
Parts No.	Stock No.	Description
oS1	49326300	Push SW., EDIT
oS2	49326300	Push SW., TAPE LENGTH
oS3	49326300	Push SW., SIDE A/B
oS4	49326300	Push SW., CHECK
oS5	49326300	Push SW., CLEAR
oS6	49326300	Push SW., REPEAT 1/ALL
oS7	49326300	Push SW., M-SCAN
oS8	49326300	Push SW., REVERSE
oS9	49326300	Push SW., FF
oS10	49326300	Push SW., AMPS DOWN
oS11	49326300	Push SW., AMPS UP
oS12	49326300	Push SW., OPEN/CLOSE
oS13	49326300	Push SW., STOP
oS14	49326300	Push SW., PLAY/PAUSE
oS15	49326300	Push SW., CONTINUE
oS16	49326300	Push SW., PROGRAM
oS17	49326300	Push SW., RANDOM
oS18	49326300	Push SW., TIME
wFL1	49719610	FL Display Tube, CC1104C

3. EXPLODED VIEW OF MECHANISM AND PARTS LIST

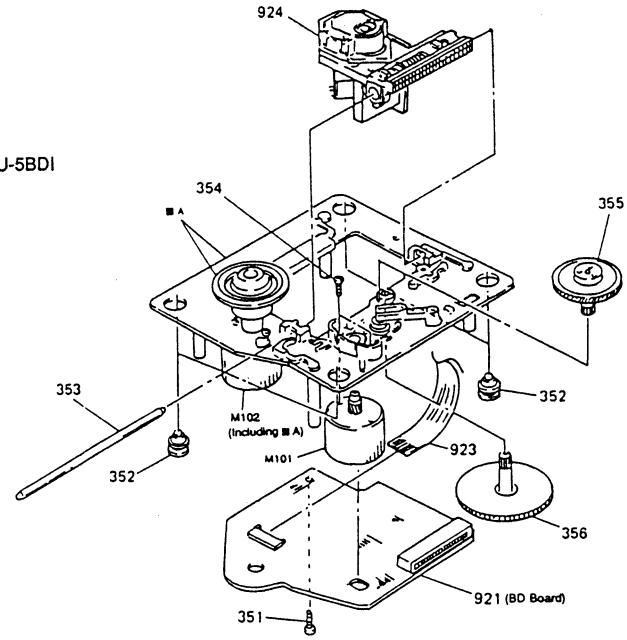
CD Mechanism Ass'y (Type No. CDM14-5BD1 Stock No. 83298600)

Note: Use this diagram for reference only.
Parts not available Separately.

3-1. CD Mechanism Ass'y



3-2. Drive Unit (BU-5BD1)



4. REPLACEMENT OF MAIN PARTS

A. CD Mechanism Assy

1) Remove the bonnet.
2) Pull out a flexible film type wire from JP2 connector on F-6800.
3) Loosen three screws Ⓐ to remove the mechanism assy. (See Fig. 4-4)
4) Remove the tray panel.

B. Tray Panel

1) Turn the OPEN/CLOSE switch ON and pull the tray out.

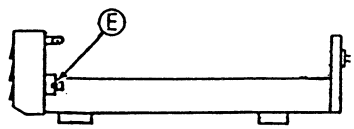
Note: When it does not OPEN, remove the mechanism assy.

2) Pull the center of the lower part of the tray panel slightly out to remove the hook and remove the tray panel right upwardly. (See Fig. 4-2)

C. F-6800 Main PC Board

1) Remove the bonnet.
2) Remove the mechanism assy.
3) Pull out a flexible film type wire from JP3 connector on F-6800.
4) Loosen two screws near cable terminal and optical digital out terminal.
5) Pull out a pin of optical digital out terminal.
6) Loosen two screws to remove the PC board.
7) Unsolder a cable at JP4 connector to remove the board.

Fig. 4-1



D. Front Panel

1) Remove the mechanism assy.
2) Loosen two screws at bottom side to remove a front panel.
3) Pull out a flexible film type wire from JP3 connector on F-6800 board.
4) Unhook two stoppers Ⓔ at both side of sub front panel to separate from chassis. (See Fig. 4-1)
5) Unhook two stoppers at top side of sub front panel and two stoppers at under side to separate from alminum front panel. (See Fig. 4-3)
6) Loosen three screws and unhook eight stoppers to remove F-6801.
7) Unsolder a cable at JP4 connector on F-6801 board.

Fig. 4-3

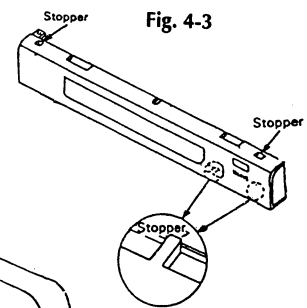


Fig. 4-2

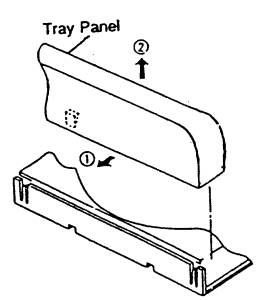
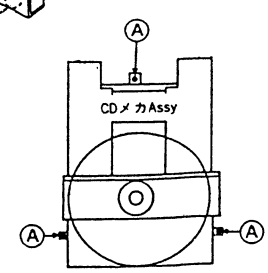
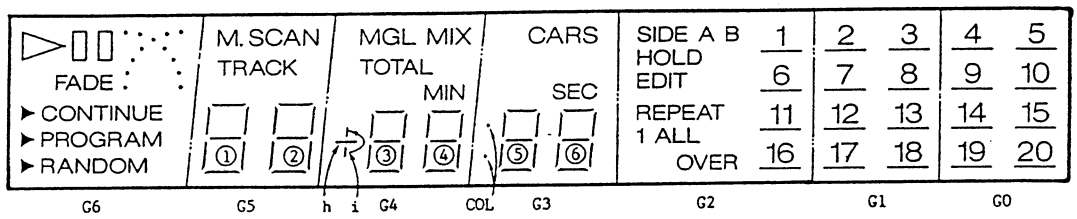
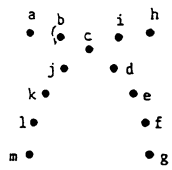


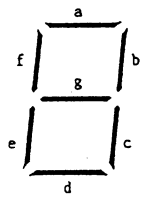
Fig. 4-4



5. PIN ASSIGNMENT OF FL DISPLAY TUBE, CC1104C







• **Pin assignment table**

Pin No.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Assignment	F	G0	G1	G2	G3	G4	G5	G6	S18	S17	S16	S15	S14	S13	S12	S11
Pin No.	17	18	19	20	21	22	23	24	25	26	27	28				
Assignment	S10	S9	S8	S7	S6	S5	S4	S3	S2	S1	S0	F				

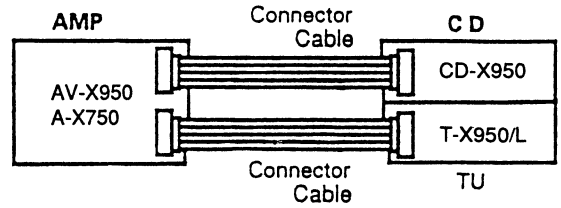
• **Assignment table of gride and anode terminal**

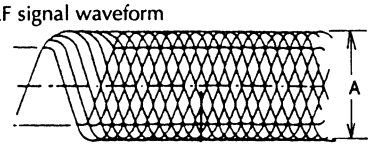
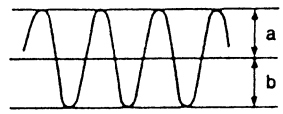
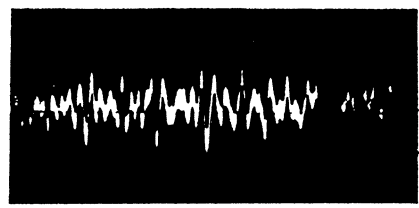
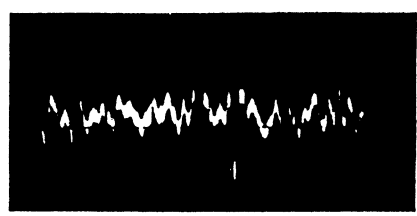
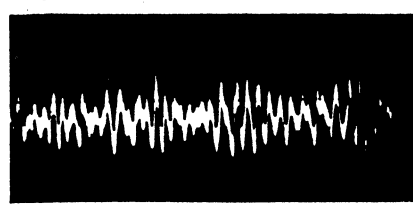
	G 0	G 1	G 2	G 3	G 4	G 5	G 6
S 0			S I D E	C A R S	MGL. MIX	M. SCAN	a
S 1	5	3	1		T O T A L	T R A C K	b
S 2	4	2	A	S E C	M I N		h
S 3	Bar of 4	Bar of 2	B	⑤ a	④ a	② a	c
S 4	9	7	H O L D	⑤ b	④ b	② b	d
S 5	Bar of 9	Bar of 7	E D I T	⑤ f	④ f	② f	e
S 6	1 5	1 3	1 1	⑤ g	④ g	② g	f
S 7	Bar of 15	Bar of 13	Bar of 11	⑤ e	④ e	② e	g
S 8	2 0	1 8	1 6	⑤ c	④ c	② c	▷
S 9	Bar of 20	Bar of 18	Bar of 16	⑤ d	④ d	② d	▶ CONTINUE
S 1 0	Bar of 19	Bar of 17	O V E R	C O L	l		▶ PROGRAM
S 1 1					h		▶ RANDOM
S 1 2	1 9	1 7	A L L	⑤ d	③ d	① d	F A D E
S 1 3	Bar of 14	Bar of 12	1 (ALL)	⑤ c	③ c	① c	□□
S 1 4	1 4	1 2	REPEAT	⑤ e	③ e	① e	m
S 1 5				⑤ g	③ g	① g	l
S 1 6	Bar of 10	Bar of 8	Bar of 6	⑤ f	③ f	① f	k
S 1 7	1 0	8	6	⑤ b	③ b	① b	j
S 1 8	Bar of 5	Bar of 3	Bar of 1	⑤ a	③ a	① a	i

6. CHECK and ADJUSTMENT

Note: 1. This adjustment require the amplifier models AV-X950 or A-X750 and tuner models T-X950 or T-X950L.
2. Connections are shown in Fig. 6-1.
3. For locations to be adjusted, see Fig. 6-2.
4. Use a test disk of EIAJ CD-1.
5. During adjustment, hold the CD mechanism Ass'y horizontal.
6. For adjustment, play the disk and then connect the probe.
7. Attach a lead wire to the test point for easy connection.

Fig. 6-1



Step	Item	Measurement location	Adjustment location	Adjustment	Setting conditions and remarks
1	Focus offset check	Connect oscilloscope at TP (RFO) and TP (VC).		RF signal waveform  A = 1.3 ± 0.3 Vp-p Check that waveform of oscilloscope is fine.	Play back 8th music of CD-1.
2	Tracking offset check	Connect oscilloscope at test points TP (TEO) and TP (VC).		 (0V) Set oscilloscope to DC rangs. Check that levels of a and b are the same.	• Connect TP (TES) to GND. • Play back 8th music of CD-1
3	V.C.O. check	Connect frequency counter to at Pin 41 (XPLCK) of flC1 of board (F-6800).		Check for 4.3218 MHz ± 30 KHz.	
4	Focus gain adjustment	Connect oscilloscope at test points TP (FEO) and TP (VC).		(Focus gain)  NG example: (DC level varies from adjusted waveform.) Undergain of focus  Overgain of focus 	• Play back 4th music (no signal) of CD-1. • Adjust RV102 so that waveform of oscilloscope becomes as shown in left.

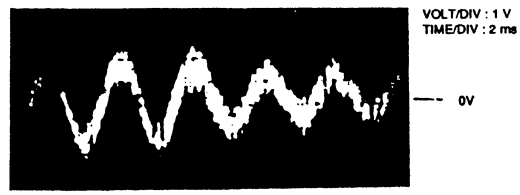
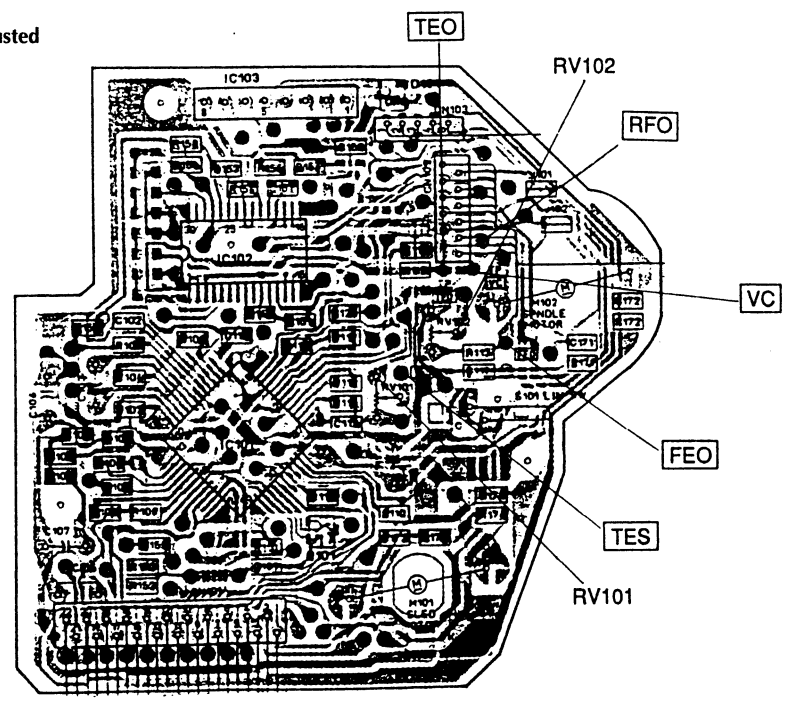
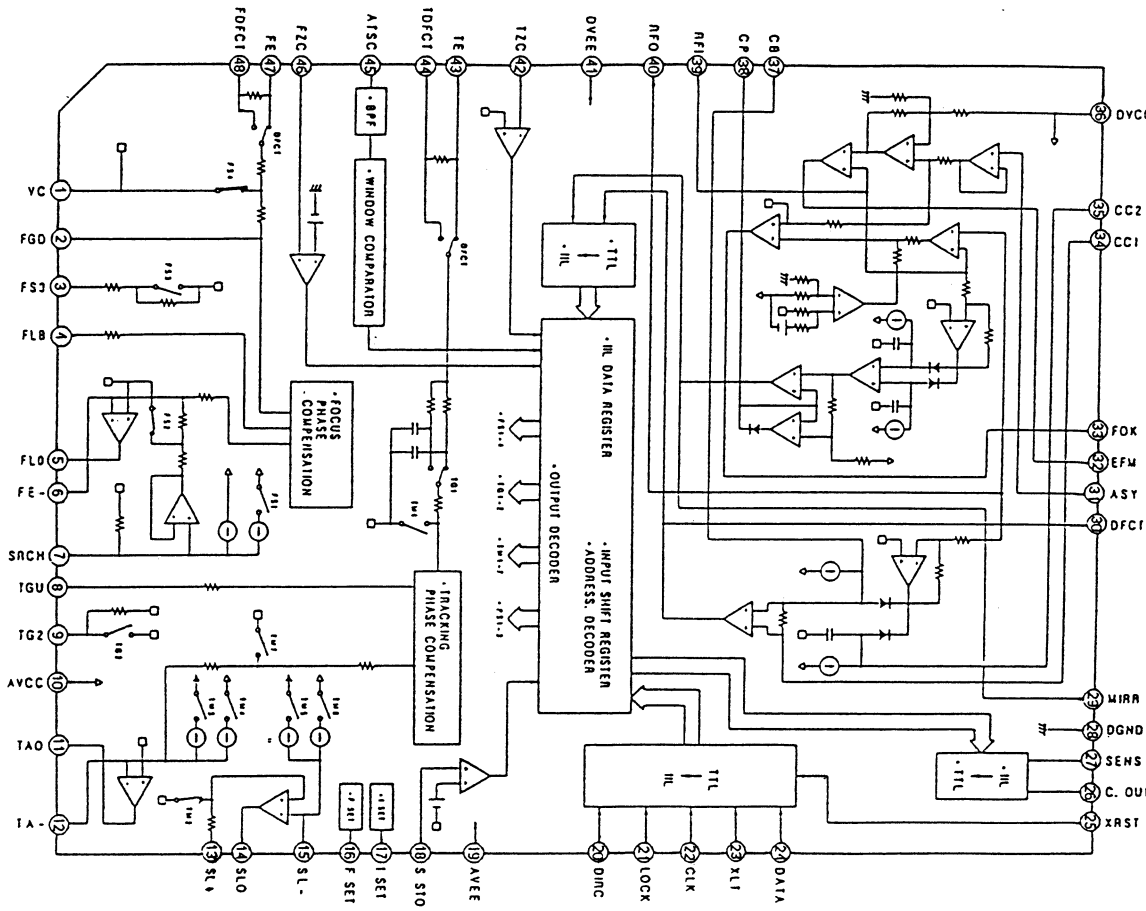
Step	Item	Measurement location	Adjustment location	Adjustment	Setting conditions and remarks
5	Tracking gain adjustment	Connect oscilloscope to test points TP (TEO) and TP (VC).	RV101	(Tracking gain) NG example: (Fundamental wave tends to appear more than adjusted waveform.) Undergain of tracking  Overgain of tracking (higher fundamental wave than that in low gain) 	• Play back 4th music (no signal) of CD-1. • Adjust RV101 so that waveform of oscilloscope becomes as shown in left.

Fig. 6-2. Locations to be adjusted



7. INTERIOR BLOCK DIAGRAM & TERMINAL FUNCTION OF ICs

- CXA1372Q (Focus/Tracking Servo)



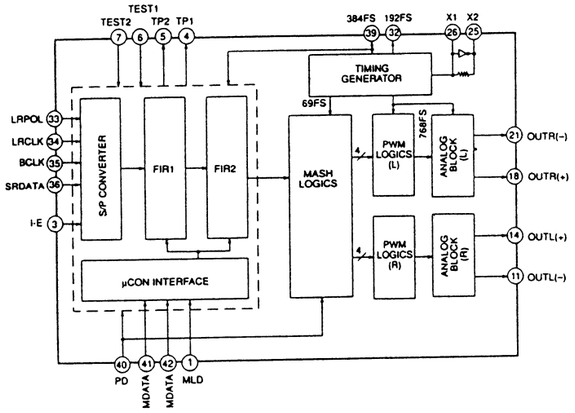
- CXA1372 Q (Focus/Tracking Servo)

Pin No.	Pin Name	Function
2	FGD	To decrease highband gain, insert a capacitor between this terminal and pin 3.
3	FS3	Switches the highband gain of focus servo by turning ON/OFF FS3.
4	FLB	This external time-constant terminal is to increase focus servo in lowband.
5	FEO	Focus drive output
6	FE⊖	Reversed input terminal of focus amplifier
7	SRCH	This external time-constant terminal is to create focus search waveform.
8	TGU	This external time-constant terminal is to switch tracking highband gain.
9	TG2	This external time-constant terminal is to switch tracking highband gain.
11	TAO	Tracking drive output
12	TA⊖	Reversed input terminal of tracking amplifier
13	SL⊕	Non-reversed input terminal of thread amplifier
14	SLO	Thread drive output
15	SL⊖	Reversed input terminal of thread amplifier
16	FSET	This terminal is to set the peak of phase compensation for focus tracking and f0 of CLV LPF.
17	ISET	Conducts a current to determine the peaks of focus search, tracking jump and thread kick.
18	SSTOP	This terminal detects ON/OFF of the limit SW to detect the innermost envelope of disk.

Pin No.	Pin Name	Function
20	DIRC	Used upon jumping 1 track. It contains a 47 kΩ pull-up resistance.
21	LOCK	Thread run-out protective circuit is actuated at "L". It contains a 47 kΩ pull-up resistance.
22	CLK	Serial data transfer clock input from CPU
23	XLT	Latch input from CPU
24	DATA	Serial data input from CPU
25	XRST	It is reset when reset input terminal is "L".
26	C.OUT	Signal output for counting the number of tracks
27	SENS	Issues outputs, e.g. FZC, AS, TZC, SSTOP and $\overline{\text{BUSY}}$ according to the command from CPU.
29	MIRR	Mirror signal output terminal
30	DFCT	Defect signal input terminal. The defect protection circuit is activated upon "H".
32	EFM	Output terminal of EFM comparator
33	FOK	FOK signal input terminal
38	CP	Non-reversed input terminal of MIRR comparator
39	RFI	Input terminal of RF summing amplifier
42	TZC	Input terminal of tracking zero cross comparator
43	TE	Tracking error signal input terminal
45	ATSC	Window comparator input terminal to detect ATSC
46	FZC	Input terminal of focus zero cross comparator
47	FE	Focus error signal input terminal

CD-X950
<Compact Disc Player>

• MN6471M/6474M (D/A Converter)



<Function> (MN6471M/6474M)

Pin No.	Pin Name	Function
1	MLD	Microcomputer command load input (load when at Ⓛ)
2	RSTB	Reset terminal (active when at Ⓛ)
3	IE	When at Ⓛ — Signal processor LSI format When at Ⓗ — I²S format
10	$AV_{DD}4$	Analog system power source terminal 4 (+5 V)
11	OUTL (−)	Left channel antiphase PWM output terminal
12	$AV_{SS}4$	Analog system ground terminal 4
13	$AV_{SS}3$	Analog system ground terminal 3
14	OUTL (+)	Left channel normal phase PWM output terminal
15	$AV_{DD}3$	Analog system power source terminal 3 (+5 V)
17	$AV_{DD}2$	Analog system power source terminal 2 (+5 V)
18	OUTR (+)	Right channel antiphase PWM output terminal
19	$AV_{SS}2$	Analog system ground terminal 2
20	$AV_{SS}1$	Analog system ground terminal 1
21	OUTR (−)	Right channel normal phase PWM output terminal
22	$AV_{DD}1$	Analog system power source terminal 1 (+5 V)
23	$DV_{DD}1$	Digital system power source terminal 1 (+5 V)
24	$DV_{SS}1$	Digital system ground terminal 1
25	X2	X'tal oscillation terminal
26	X1	X'tal oscillation terminal
28	$DV_{DD}2$	Digital system power source terminal 2 (+5 V)
29	$DV_{SS}2$	Digital system ground terminal 2
30	NSUB	Connected to $D\text{-}V_{DD}$ (silicone PCB potential stabilizer terminal)
32	192FS	192 FS (=8.4672 MHz) output terminal
33	LRPOL	Left/right channel switching terminal (Ⓗ is left channel, Ⓛ is right channel)
34	LRCLK	LRCLK input terminal LR-POL when at Ⓗ: Ⓗ is left channel data input, Ⓛ is right channel data input LR-POL when at Ⓛ: Ⓛ is right channel data input, Ⓗ is left channel data input
35	BCLK	Serial input bit clock
36	SRDATA	Serial input data (digital) input terminal
37	$DV_{SS}3$	Digital system ground terminal 3
38	DV_{DD}	Digital system power source terminal (COM potential stabilizer terminal)
39	384FS	384 FS (16.9344 MHz) output terminal
40	PD	Power down terminal (Ⓛ is active)
41	MDATA	Microcomputer command data input terminal
42	MCLK	Microcomputer command clock input terminal

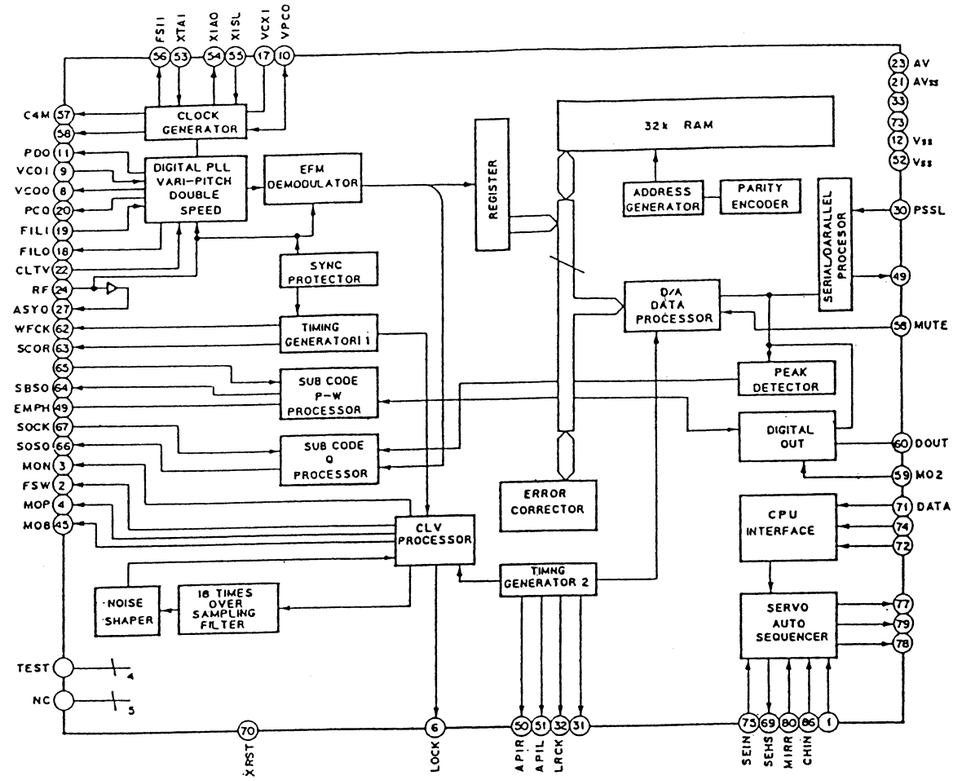
• CXP50116 (key Matrix)

LEVEL / PORT	0 V	1 V	2 V	3 V	4 V
AD0/PI0 (p35)	FLDUTY 4/16	FLDUTY 6/16	FLDUTY 8/16	FLDUTY 10/16	FLDUTY 12/16
AD1/PI1 (p36)	10/0	1	2	3	4
AD2/PI2 (p37) M	5	6	7	8	9
AD3/PI3 (p38)	EDIT	TAPE LENGTH	SIDE A, B	≧10	Vari Pitch On/Off
AD4/PB0 (p39)	CHECK	CLEAR	REPEAT 1/ALL	M. SCAN	Vari Pitch Down
AD5/PB1 (p40)	FR ◁◁	FF ▷▷	TRACK DOWN ⋈◁	TRACK UP ▷⋈	Vari Pitch UP
AD6/PB2 (p41)	OPEN/ CLOSE	STOP □	▷ / II	Power On	Power Off
AD7/PB3 (p42)	CONTINUE	PROGRAM	RANDOM	TIME DIS. Sel	PL ALL On

• CXP50116 (System Control)

Pin No.	Pin Name	I/O	Function	Active
29	SCOR	I	Subcode sync input	L
32	$\overline{RST}$	I	Power ON reset	L
34	Voo		Voo	
35 36 37 38 39 40 41 42	AD0 AD1 AD2 AD3 AD4 AD5 AD6 AD7	AD AD AD AD AD AD AD AD	Tact input terminal	H
43	$\overline{EC}$	I	Event counter input terminal connected to Voo.	
44	SUBQ	I	Subcording Q-channel signal input from the CXD2500AQ	L
46	SQSO	I	Sub Q 80 bits, PCM peak and level data 16 bits input.	
50	DATA	O	Serial data output	L
51	MUTE	O	MUTE and reset at H and L, respectively	H
52	XLAT	O	Latch output	L
53	XRST	O	Reset output terminal	L
54	$\overline{LDON}$	O	Laser ON output	L
55	GFS	I	GFS input	
56	FOK	I	Focus OK signal input terminal	
57	SENS	I	FZC, AS, TZC and SSTOP output	
62	SSB IN	I	SSB input	L
63	SSB OUT	O	SSB output	H
64	MLD	O	Load output for controlling MASH level	L
65	MDATA	O	Data output for controlling MASH level	L
66	MCLK	O	Clock output for controlling MASH level	L
69	CLOSE SW	I	Tray close SW input	L
70	OPEN SW	I	Tray open SW input	L
71	Vss		GND	
72	XTAL		Output from clock generator circuit	
74	EXTAL		Input to clock generator circuit	
75	Vref		Connected to Voo	
79	OPEN	O	Motor open output	H
80	CLOSE	O	Motor close output	H

• CXD2500AQ (Digital Signal Processing)



♦ Terminal Function <CXD2500AQ>

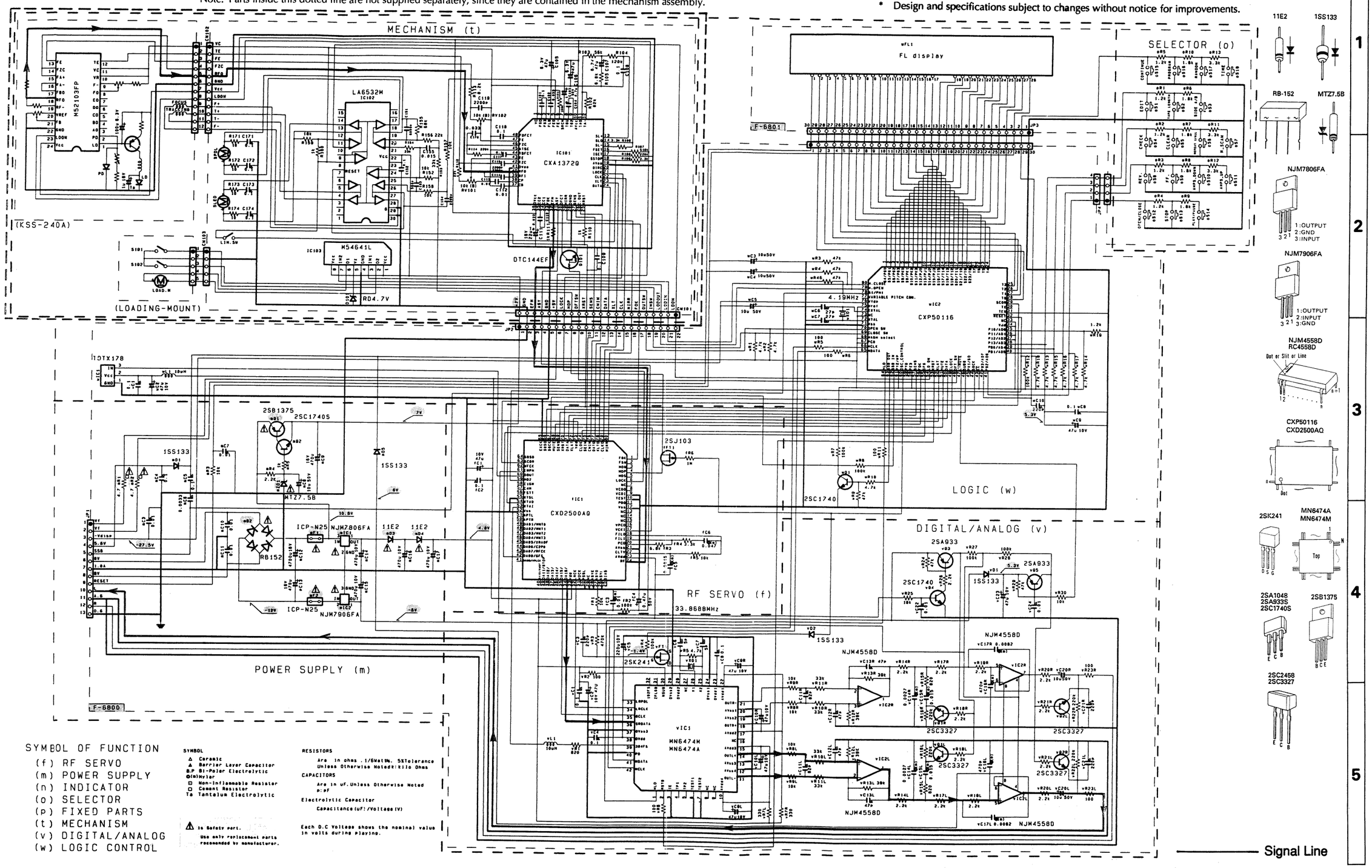
Pin No.	Pin Name	I/O	Function
1	FOK	I	Focus OK input terminal
2	FSW	O	Time-constant switching output for the output filter of spindle motor
3	MON	O	ON/OFF control output of spindle motor
4	MDP	O	Drive output of spindle motor. Rough control in CLV.S mode and phase control in CLV.P mode
5	MDS	O	Drive output of spindle motor Speed control in CLV.P mode
6	LOCK	O	This pin samples GFS signal by WFCK/16 and, if "H", issues "H". If "L" occurs consecutively 8 cycles, "L" is output.
8	VCOO	O	VCO output. When it is locked at EFM signal, f = 8.6436 MHz.
9	VCOI	I	VCO input
10	TEST	I	(0V)
11	PDO	O	Charge pump output for pulling up analog EFM signal
16	VPCO	O	PLL charge pump output for variable pitch
17	VCKI	O	Clock input for variable pitch. f = 19.0934MHz
18	FILO	O	Filter output for master PLL
19	FILI	I	Filter input for master PLL
24	RF	I	EFM signal input from EF amplifier
27	ASYO	O	Full swing output of EFM signal

Pin No.	Pin Name	I/O	Function
59	MD2	I	ON/OFF control of digital output. ON and OFF at H and L, respectively.
60	DOUT	O	Digital output
61	EMPH	O	Issues H or L when replaying disk is with or without emphasis, respectively.
68	MUTG	I	Muting input. MUTG is "T" normal state or no sound state when ATTM of the internal register is "L" or "H", respectively.
69	SENS	O	Issues the output of internal state according to the address.
70	XRST	I	System reset input. To be reset at "L".
71	DATA	I	Serial data input from CPU
72	XLAT	I	Latch input from CPU. Data (serial data from CPU) is latched to each register at the fall of the signal.
73	Voo	—	Power supply (+5 V)
74	CLOK	I	Serial data transfer clock input from CPU. It latches data at the rise of locking signal.
75	SEIN	I	Sense input from SSP.
76	CNIN	I	Tracking pulse input
77	DATO	O	Serial data output to SSP
78	XLTO	O	Serial data latch output to SSP
79	CLKO	O	Serial data transfer clock output to SSP
80	MIRR	I	Mirror signal input

8. SCHEMATIC DIAGRAM

*Note: Parts inside this dotted line are not supplied separately, since they are contained in the mechanism assembly.

* Design and specifications subject to changes without notice for improvements.



SYMBOL OF FUNCTION
- (f) RF SERVO
- (m) POWER SUPPLY
- (n) INDICATOR
- (o) SELECTOR
- (p) FIXED PARTS
- (t) MECHANISM
- (v) DIGITAL/ANALOG
- (w) LOGIC CONTROL

SYMBOL
- Δ Ceramic
- ▲ Barrier Layer Capacitor
- B.P Bi-Polar Electrolytic
- Ⓜ(m)Mylar
- ▯ Non-Inflammable Resistor
- ▯ Cement Resistor
- Ta Tantalum Electrolytic

⚠ is Safety part.
Use only replacement parts recomended by manufacturer.

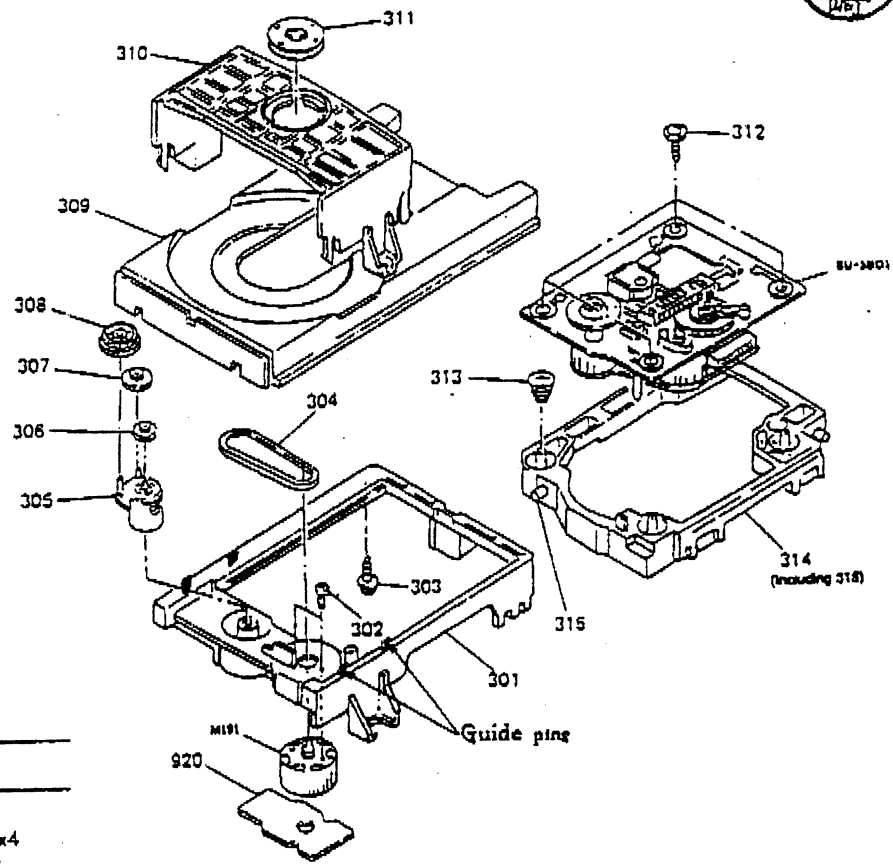
RESISTORS
Are in ohms .1/6Watt%. 5%Tolerance
Unless Otherwise Noted:K:Kilo Ohms

CAPACITORS
Are in uF.Unless Otherwise Noted
P:pF

Electrolytic Capacitor
Capacitance(uF):Voltage(V)

Each D.C Voltage shows the nominal value in volts during playing.

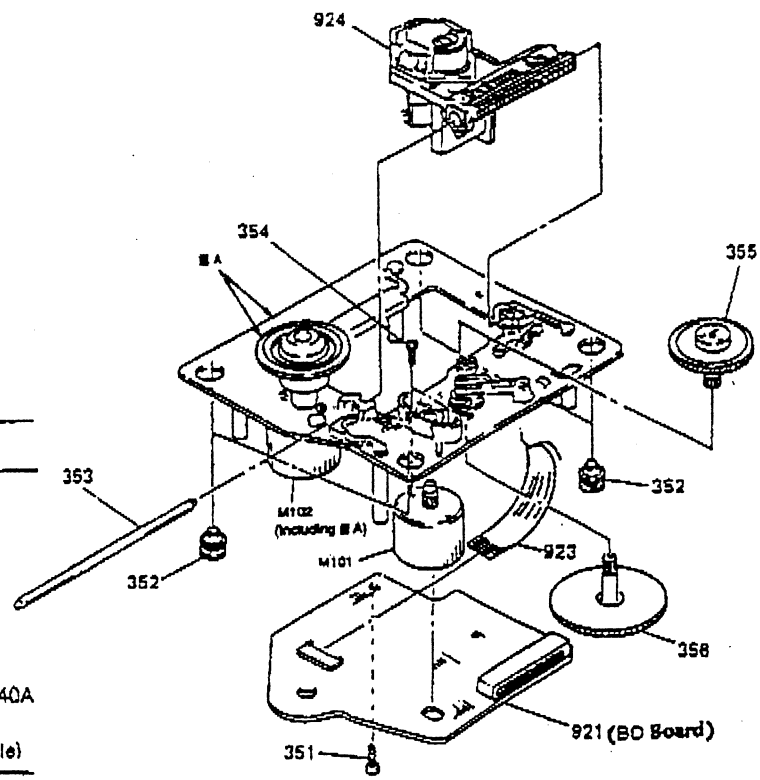
3-1. MD Section (CDM14-5BD1)



<Parts List> (CDM14-5BD1)

Parts No.	Stock No.	Description
301	36245800	Chassis
302	36246400	Screw, M2.6x4
303	36248200	Screw, M3x8
304	36248400	Belt
305	36248800	Cam
306	36248500	Pulley (S)
307	36248300	Gear (C)
308	36248600	Gear (PL)
309	36245900	Disc Table
310	36248900	Holder (MG)
311	36250500	Magnet
312	36246200	Screw, M2.6x6
313	36247800	Spring (B)
314	36246100	Holder (BU)
315	36248700	Axle (Cam)
920	36247700	Loading PCB
M191	36250800	Motor (L) Ass'y

3-2. Drive Unit (BU-5BD1)



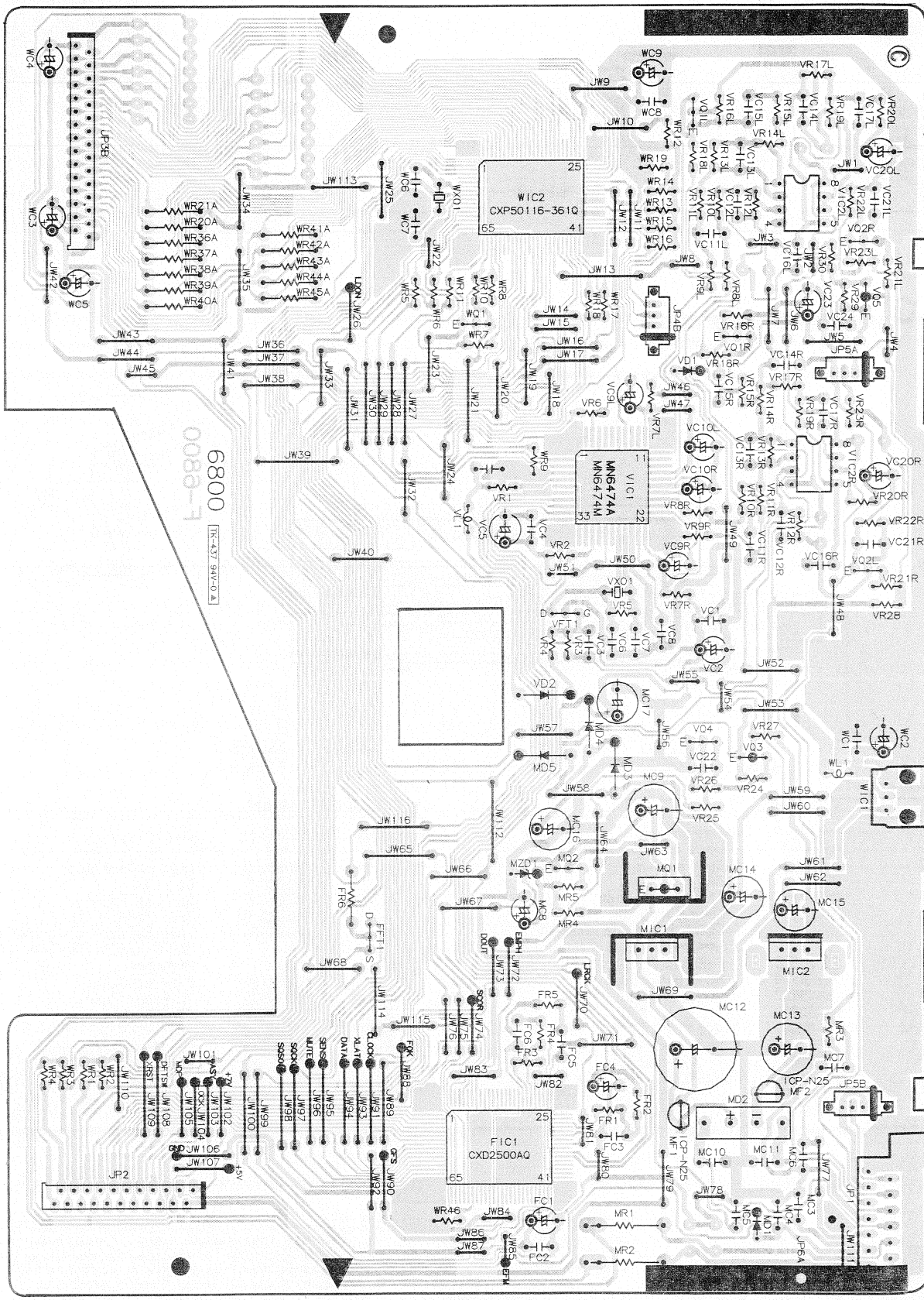
<Parts List> (BU-5BD1)

Parts No.	Stock No.	Description
351	36246500	Screw, M2.6x8
352	36246000	Insulator (A)
353	36248000	Sled shaft
354	36246300	Screw, M2x3
355	36248100	Gear (M)
356	36247900	Gear (P)
921	36250900	BD MOUNT
923	36247600	Wire (12P)
⚠ 924	36247100	Optical Pick-up Ass'y KSS-240A
M101	36251000	Motor Ass'y (Sled)
M102	36251100	Motor Ass'y with ■ (Spindle)

9. PARTS LOCATION ON BOARD

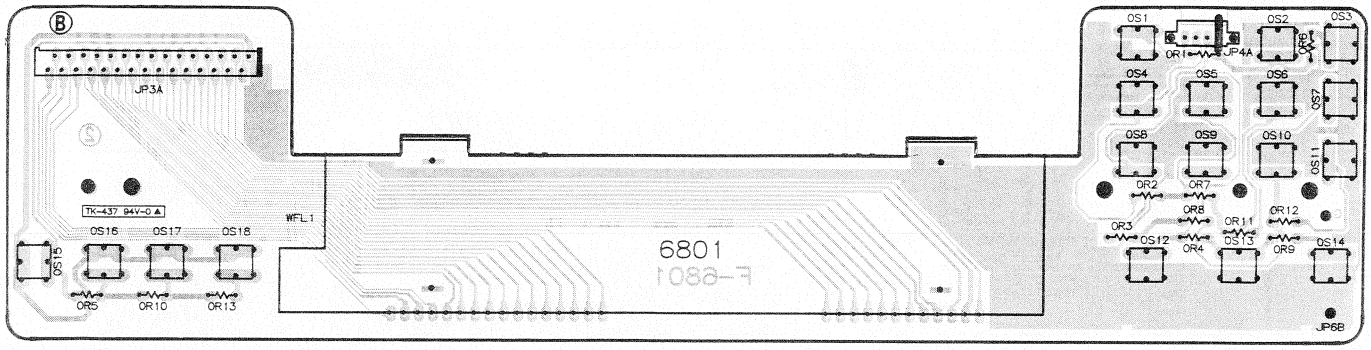
9-1. F-6800 Main Board

Component Side

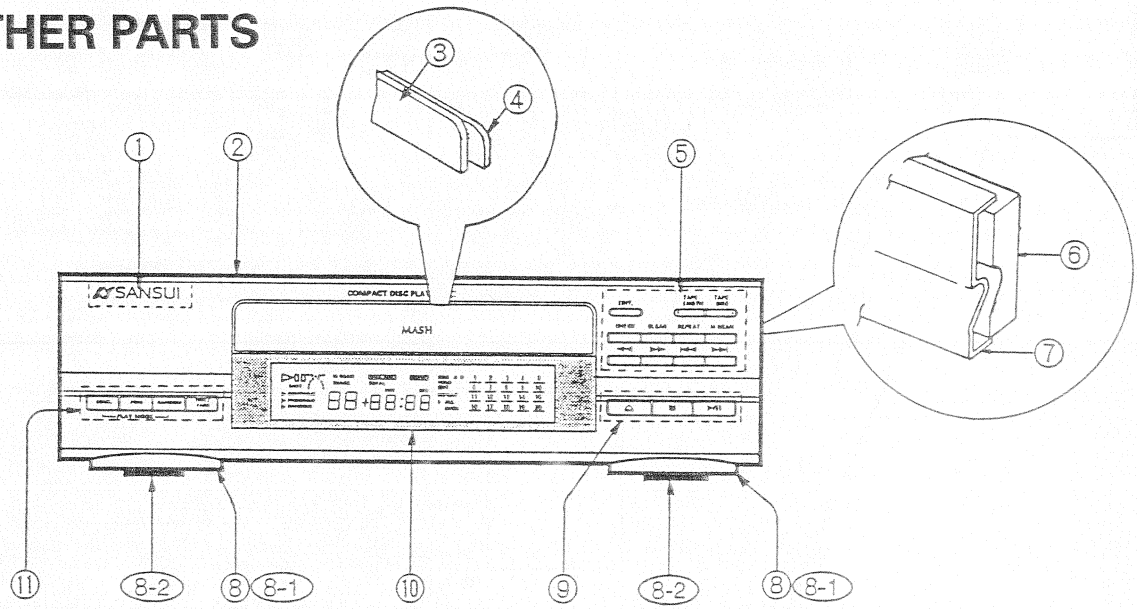


9-2. F-6801 Control Switches Board

Component Side



10. OTHER PARTS



• Parts List of Black Model

Parts No	Stock No	Description
1	05095600	Logo type Badge
2	05091100	Bonnet
3	05094300	AL-Tray Panel
4	05094100	Tray Panel Base
5	05093900	Knob,EDIT,TAPE L/S,CHECK, CLEAR,REPEAT,M SCAN, ◄◄, ►►, ⏮, ⏭
6	05093600	P-Front Panel
7	05128400	AL-Front Panel
8	05149900	Foot Assy
8-1	05095700	Foot
8-2	05102300	Leg Sheet(Front)
	27902510	Leg Sheet(Rear)
9	05093700	Knob,EJECT,STOP,PLAY/PAUSE
10	05094000	DS Plate
11	05093800	Knob,CONT.,PGM,RANDOM, TIME/FADE

• Parts of Gold Model

Parts No	Stock No	Description
1	05095600	Logo type Badge
2	05104800	Bonnet
3	05104600	AL-Tray Panel
4	05104200	Tray Panel Base
5	05104500	Knob,EDIT,TAPE L/S,CHECK, CLEAR,REPEAT,M SCAN, ◄◄, ►►, ⏮, ⏭
6	05104100	P-Front Panel
7	05159500	AL-Front Panel
8	05149900	Foot Assy
8-1	05095700	Foot
8-2	05102300	Leg Sheet(Front)
	27902510	Leg Sheet(Rear)
9	05104300	Knob,EJECT,STOP,PLAY/PAUSE
10	05094000	DS Plate
11	05104400	Knob,CONT.,PGM,RANDOM, TIME/FADE

SANSUI

SANSUI ELECTRIC CO., LTD.: New River Bldg. 10-14, Shinkawa 1-chome, Chuo-ku, Tokyo 104, Japan
SANSUI USA INC.: PHONE: (03) 5566-1024 / FAX: 03-5566-1027 (International Division)
1290 Wall Street West, Lyndhurst, New Jersey 07071 U.S.A.
SANSUI DEUTSCHLAND G.M.B.H.: 17150 South Margay Ave. Carson, California 90746 U.S.A.
Paul-Ehrlich-Strasse 8, 6074 Rödermark 2, F. R. Germany
山水電気株式会社 東京都中央区新川1-10-14 (〒104)

(SM2-311) Printed in Japan (1992.02.C) <Stock No. 36565900>